

Philco



**MINI
SYSTEM
PH1100**

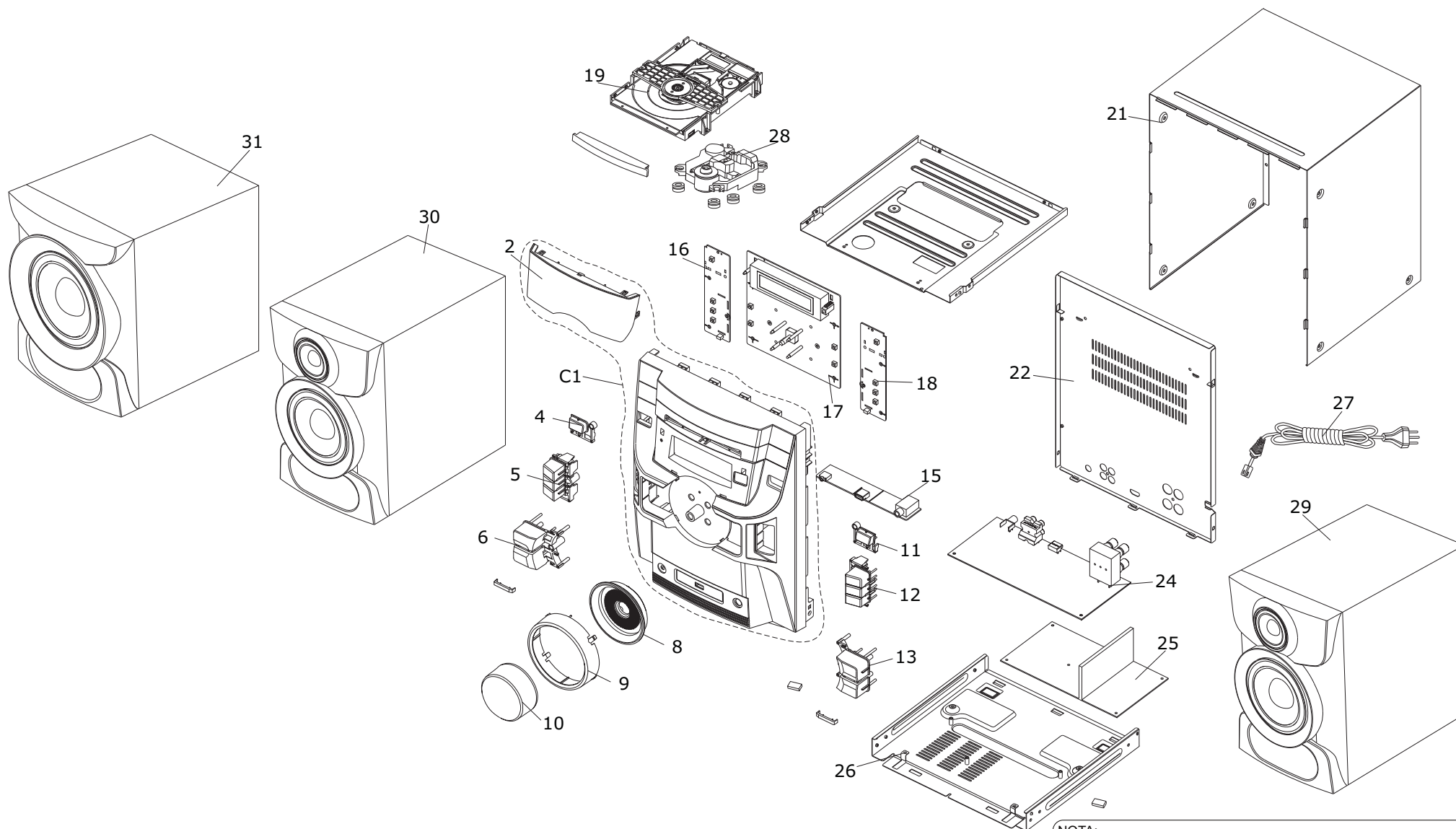
0800 415300

suporte.tecnico@britania.com.br

VISTA EXPLODIDA

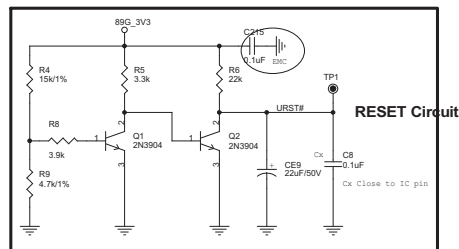
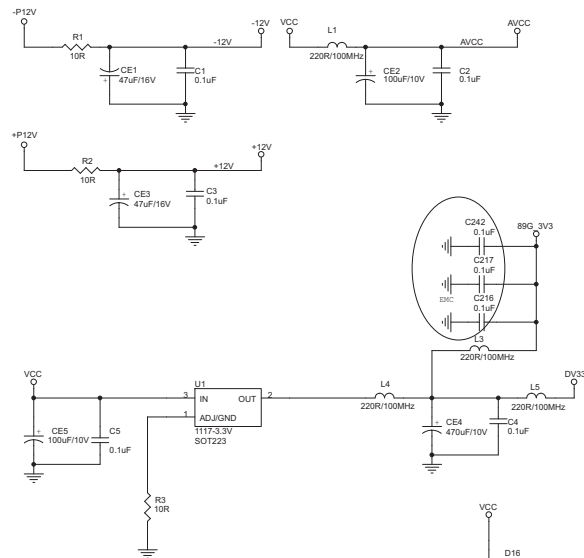
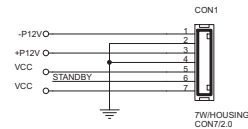
Acesse a LISTA BÁSICA em www.telecontrol.com.br
e consulte os códigos das peças pela referência.

PRODUTO	CÓDIGO	TENSÃO	PADRÃO E Nº SÉRIE
MINI SYSTEM PH1100	056603103ATX	BIV	0 00 00000 00 V115X
(X) - O último algarismo nas colunas "Código" e "Padrão Nº Série" significa a versão do aparelho.			



NOTA:
- Esta lista está sujeita a alterações sem aviso prévio, favor consultar o site www.telecontrol.com.br para informações atualizadas.
- Em caso de dúvidas entrar em contato com o **Suporte Técnico**.
- Somente os itens que possuem códigos serão fornecidos.

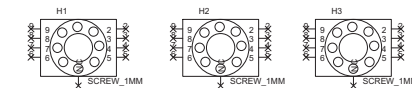
- 1 INDEX & POWER / RESET
- 2 MT1389GH LQFP176
- 3 SDRAM & FLASH & MOTOR
- 4 AUDIO OUT
- 5 VIDEO I/F
- 6 Digital system process
- 7 Digital Amplifier

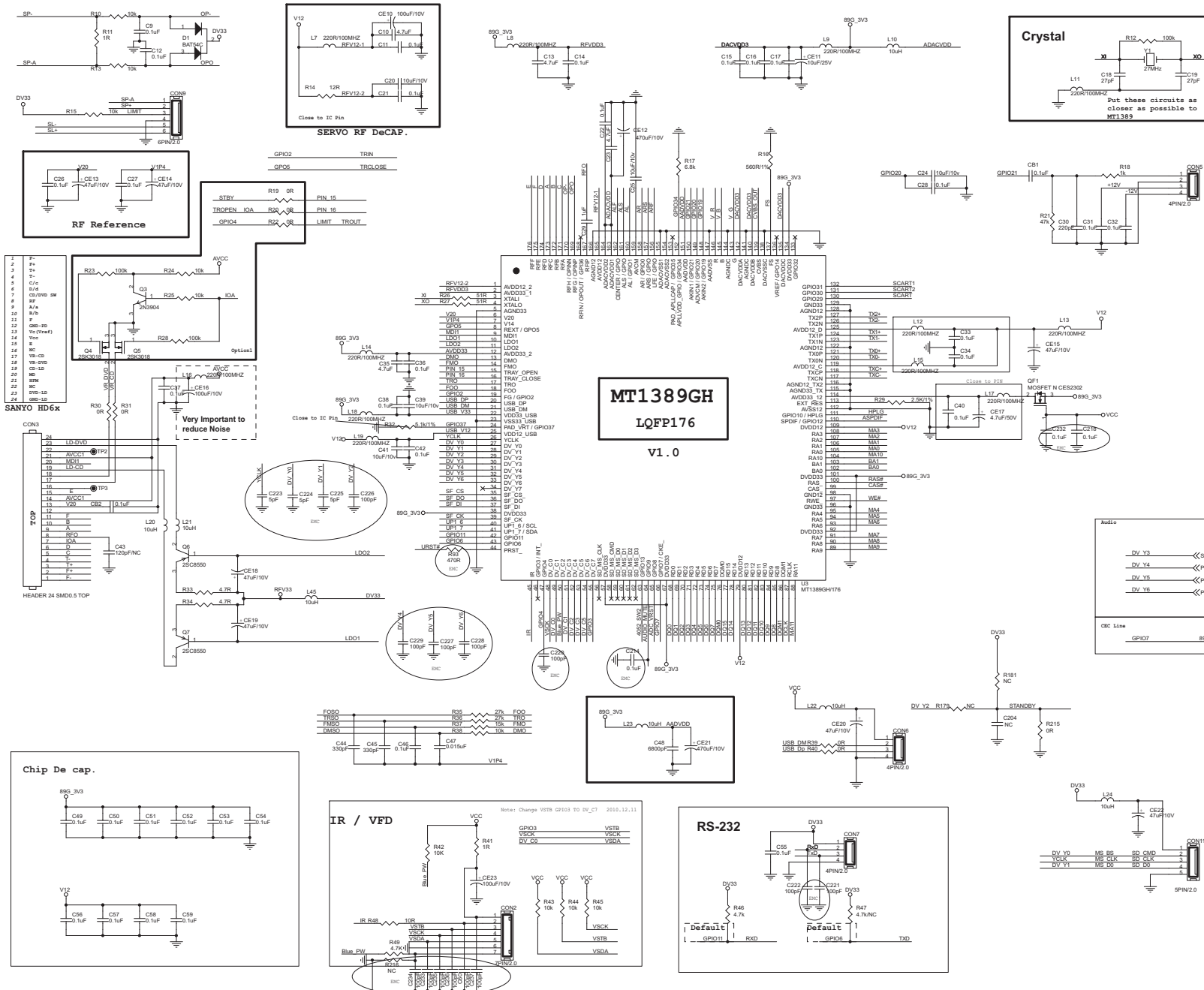


OFF-PAGE CONNECTION

URST#	URST#	[2]
+12V	+12V	[2,4,5]
-12V	-12V	[2,4]
AVCC	AVCC	[2]
VCC	VCC	[2,3,4,5,6]
DV33	DV33	[2,3,5]
STANDBY	STANDBY	[2]
RFV33	RFV33	[2]
89G_3V3	89G_3V3	[2,4,5]
V12	V12	[2]
-12V	-12V	[2,4]
GND	GND	[2,3,4,5,6,7]

Power





OFF-PAGE CONNECTION

DQIO_10	<<>> DQIO_10	[3]
MAIO_11	<<>> MAIO_11	[3]
DMIO_11	<<>> DMIO_11	[3]
BAIO_11	<<>> BAIO_11	[3]
DCLK	<<>> DCLK	[3]
RASP	<<>> RASP	[3]
CASP	<<>> CASP	[3]
WE#	<<>> WE#	[3]

DRAM I/F

SE_CK	<<>> SE_CK	[3]
SE_CS	<<>> SE_CS	[3]
SE_DO	<<>> SE_DO	[3]
SE_DI	<<>> SE_DI	[3]

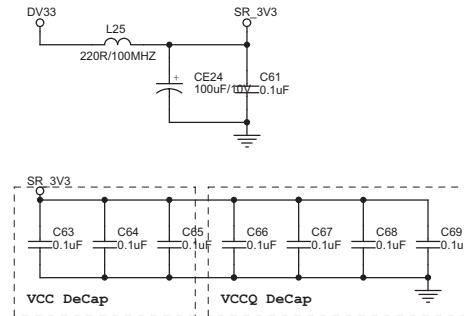
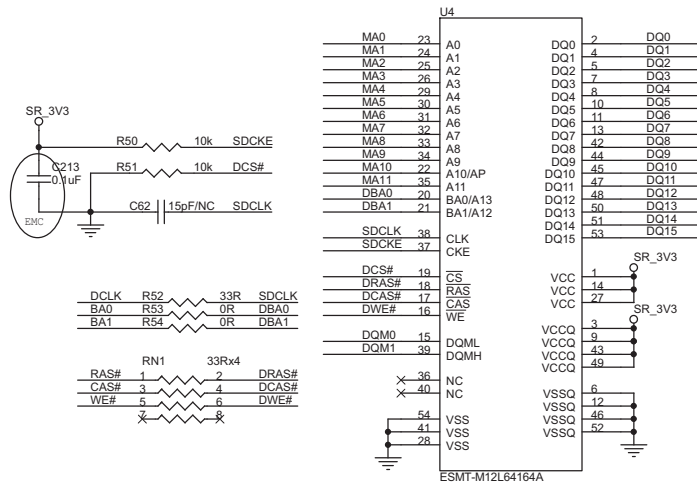
S-FLASH

CVBS_OUT	<<>> CVBS_OUT	[3]
V_B	<<>> V_B	[3]
V_B	<<>> V_B	[3]
SCART1	<<>> SCART1	[3]
SCART2	<<>> SCART2	[3]
SCART3	<<>> SCART3	[3]

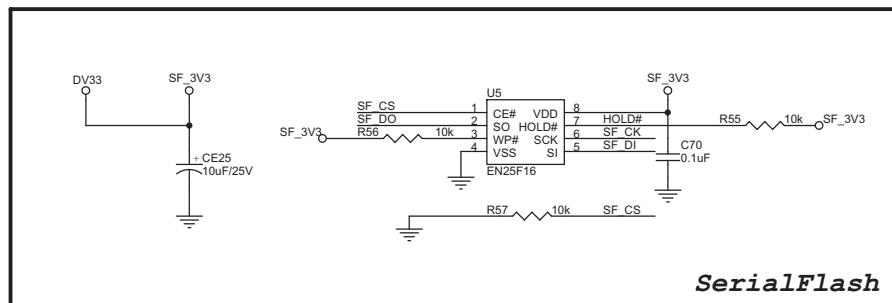
VIDEO I/F

PCIN_1	<<>> PCIN_1	[3]
PCIN_2	<<>> PCIN_2	[3]
PCIN_3	<<>> PCIN_3	[3]
PCIN_4	<<>> PCIN_4	[3]
PCIN_5	<<>> PCIN_5	[3]
PCIN_6	<<>> PCIN_6	[3]
PCIN_7	<<>> PCIN_7	[3]
PCIN_8	<<>> PCIN_8	[3]
PCIN_9	<<>> PCIN_9	[3]
PCIN_10	<<>> PCIN_10	[3]
PCIN_11	<<>> PCIN_11	[3]
PCIN_12	<<>> PCIN_12	[3]
PCIN_13	<<>> PCIN_13	[3]
PCIN_14	<<>> PCIN_14	[3]
PCIN_15	<<>> PCIN_15	[3]
PCIN_16	<<>> PCIN_16	[3]
PCIN_17	<<>> PCIN_17	[3]
PCIN_18	<<>> PCIN_18	[3]
PCIN_19	<<>> PCIN_19	[3]
PCIN_20	<<>> PCIN_20	[3]
PCIN_21	<<>> PCIN_21	[3]
PCIN_22	<<>> PCIN_22	[3]
PCIN_23	<<>> PCIN_23	[3]
PCIN_24	<<>> PCIN_24	[3]
PCIN_25	<<>> PCIN_25	[3]
PCIN_26	<<>> PCIN_26	[3]
PCIN_27	<<>> PCIN_27	[3]
PCIN_28	<<>> PCIN_28	[3]
PCIN_29	<<>> PCIN_29	[3]
PCIN_30	<<>> PCIN_30	[3]
PCIN_31	<<>> PCIN_31	[3]
PCIN_32	<<>> PCIN_32	[3]
PCIN_33	<<>> PCIN_33	[3]
PCIN_34	<<>> PCIN_34	[3]
PCIN_35	<<>> PCIN_35	[3]
PCIN_36	<<>> PCIN_36	[3]
PCIN_37	<<>> PCIN_37	[3]
PCIN_38	<<>> PCIN_38	[3]
PCIN_39	<<>> PCIN_39	[3]
PCIN_40	<<>> PCIN_40	[3]
PCIN_41	<<>> PCIN_41	[3]
PCIN_42	<<>> PCIN_42	[3]
PCIN_43	<<>> PCIN_43	[3]
PCIN_44	<<>> PCIN_44	[3]
PCIN_45	<<>> PCIN_45	[3]
PCIN_46	<<>> PCIN_46	[3]
PCIN_47	<<>> PCIN_47	[3]
PCIN_48	<<>> PCIN_48	[3]
PCIN_49	<<>> PCIN_49	[3]
PCIN_50	<<>> PCIN_50	[3]
PCIN_51	<<>> PCIN_51	[3]
PCIN_52	<<>> PCIN_52	[3]
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PCIN_72	<<>> PCIN_72	[3]
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PCIN_86	<<>> PCIN_86	[3]
PCIN_87	<<>> PCIN_87	[3]
PCIN_88	<<>> PCIN_88	[3]
PCIN_89	<<>> PCIN_89	[3]
PCIN_90	<<>> PCIN_90	[3]
PCIN_91	<<>> PCIN_91	[3]
PCIN_92	<<>> PCIN_92	[3]
PCIN_93	<<>> PCIN_93	[3]
PCIN_94	<<>> PCIN_94	[3]
PCIN_95	<<>> PCIN_95	[3]
PCIN_96	<<>> PCIN_96	[3]
PCIN_97	<<>> PCIN_97	[3]
PCIN_98	<<>> PCIN_98	[3]
PCIN_99	<<>> PCIN_99	[3]
PCIN_100	<<>> PCIN_100	[3]

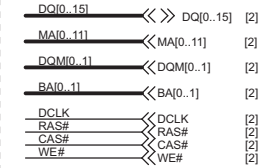
AL	PCM_IN1	[6]
AR	PCM_IN2	[6]
ALS	DSP_LRCK	[4,6]
ARS	DSP_BCLK	[4,6]
ALF	PCM_IN0	[4,6]
ARF	MCLK	[4,6]
ASPDIF	ASPDIF	[5]



SDR_DRAM (Dual Layout)



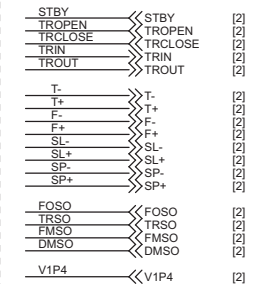
OFF-PAGE CONNECTION



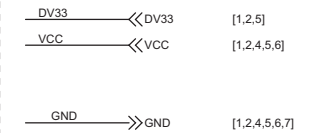
DRAM I/F



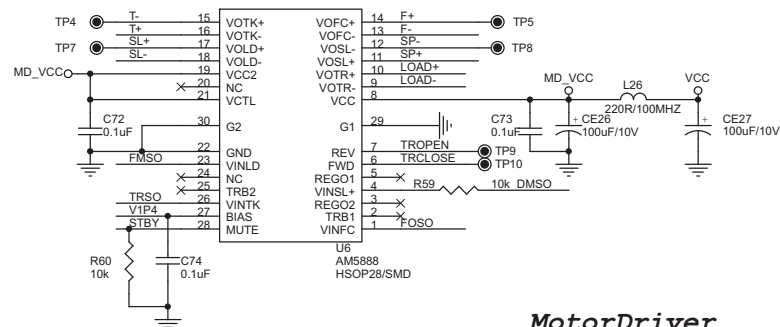
S-FLASH



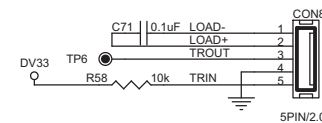
SERVO I/F



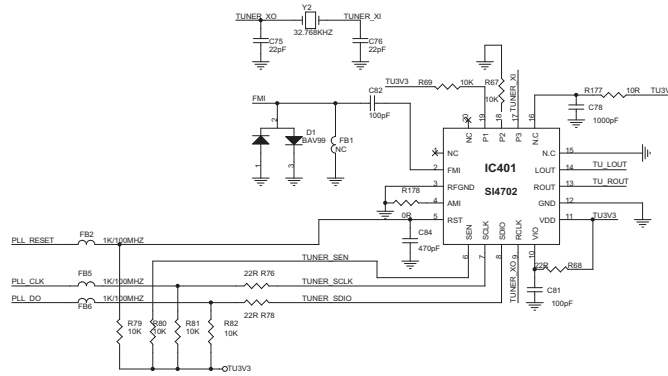
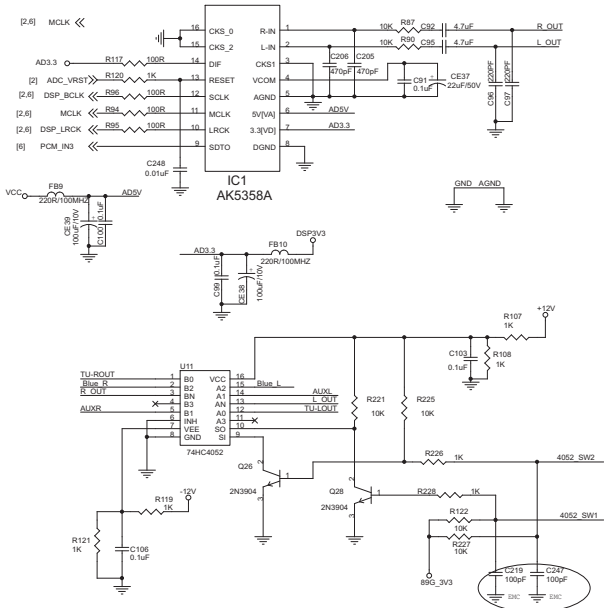
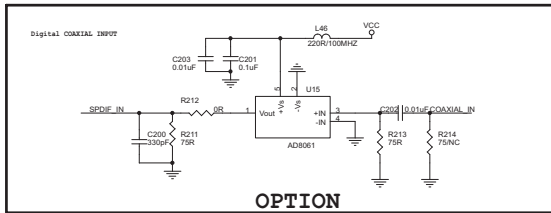
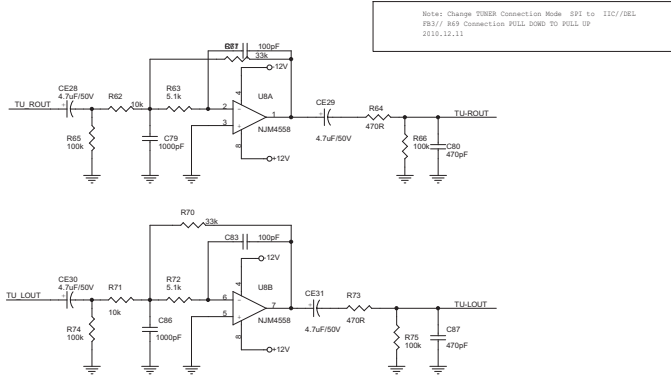
Power



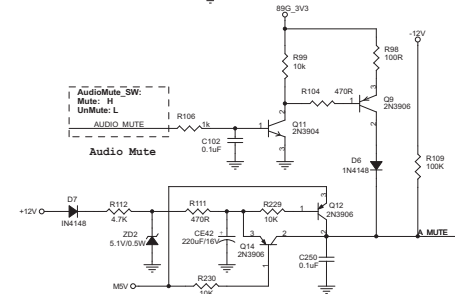
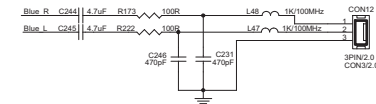
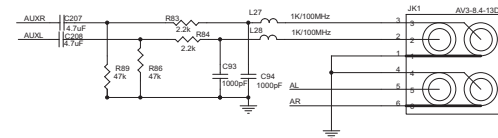
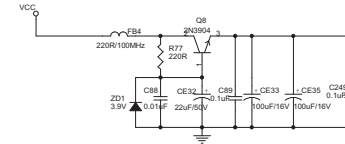
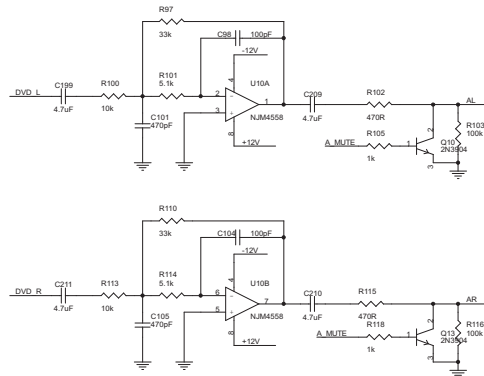
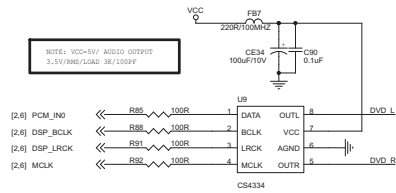
MotorDriver



REFERÊNCIA ESQUEMA=4



Option



OFF-PAGE CONNECT

A/D & D/A CONNECT

ADC_VRST	↔	ADC_VRST	[2]
PCM_IN3	↔	PCM_IN3	[6]
DSP_LRCK	↔	DSP_LRCK	[2,6]
DSP_BCLK	↔	DSP_BCLK	[2,6]
MCLK	↔	MCLK	[2,6]
4052_SW1	↔	4052_SW1	[2]
COAXIAL_IN	↔	COAXIAL_IN	[2]
SPDIF_IN	↔	SPDIF_IN	[2]

TUNER&AUDIO CONNECT

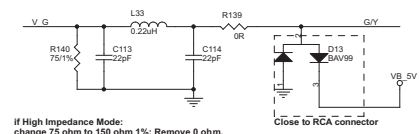
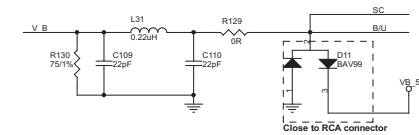
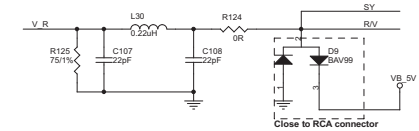
PLL_RESET	⇐ PLL_RESET	[2]
PLL_CLK	⇐ PLL_CLK	[2]
PLL_DO	⇐ PLL_DO	[2]

AUDIO_MUTE << AUDIO_MUTE [2]
AL << AL [5]
AR << AR [5]

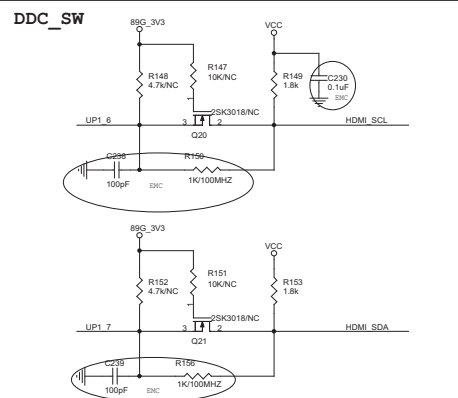
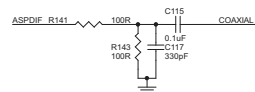
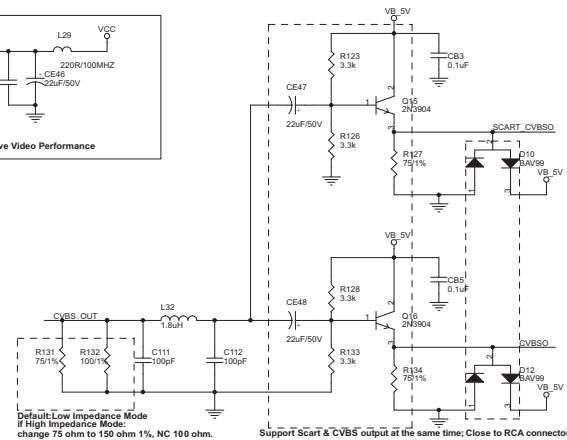
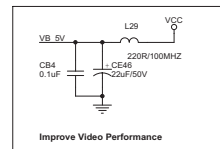
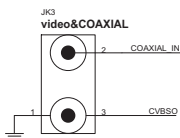
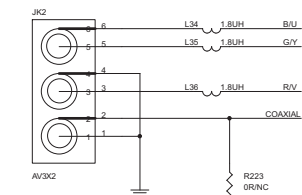
POWER CONNECT

GND	↔ GND	[1,2,3,5,6]
VCC	↔ VCC	[1,2,3,5,6]
+12V	↔ +12V	[1,2,5]
-12V	↔ -12V	[1,2]
DSP3V3	↔ DSP3V3	[6]
89G_3V3	↔ 89G_3V3	[1,2,5]

Low Impedance Mode

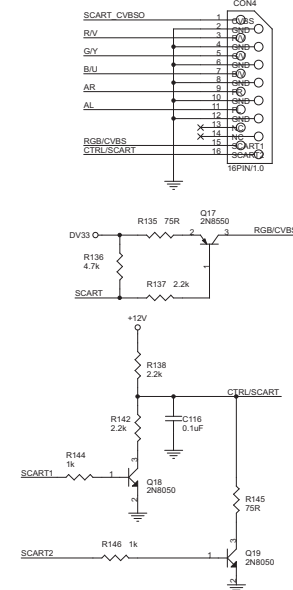


If High Impedance Mode:
change 75 ohm to 150 ohm 1%; Remove 0 ohm.

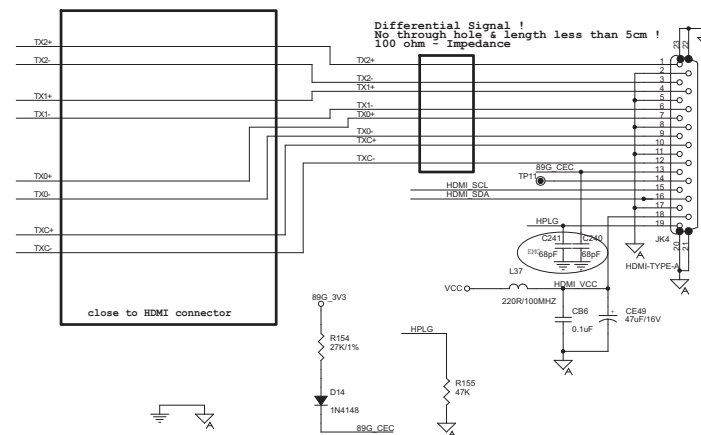
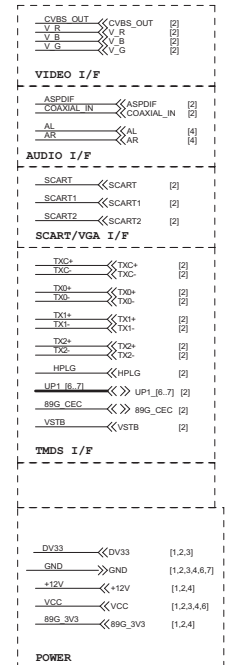


SCART1 SCART_P8HVSINCP
SCART2 SCART_P16HVSINCP

SCART/VGA CONNECTOR



OFF-PAGE CONNECTION



IC VTL Surround mode

Channel 1:left
Channel 2:right
Channel 3:left surround
Channel 4:right surround
Channel 5:center
Channel 6:LFE

PCM_OUT CONNECT

[7] EAPD	>>	EAPD
[7] OUT1A	>>	OUT1A
[7] OUT2A	>>	OUT2A
[7] OUT3A	>>	OUT3A
[7] OUT4A	>>	OUT4A
[7] OUT5A	>>	OUT5A
[7] OUT6A	>>	OUT6A
[7] OUT6B	>>	OUT6B
[7] AMP3V3	>>	AMP3V3

DSP_DATA CONNECT

[2,4] PCM_IN0	>>	PCM_IN0
[2] PCM_IN1	>>	PCM_IN1
[2] PCM_IN2	>>	PCM_IN2
[4] PCM_IN3	>>	PCM_IN3
[2,4] DSP_LRCK	>>	DSP_LRCK
[2,4] DSP_BCLK	>>	DSP_BCLK
[2] DSP_RESET	>>	DSP_RESET
[2] I2C/SDA	>>	I2C/SDA
[2] I2C/SCL	>>	I2C/SCL
[2,4] MCLK	>>	MCLK
[4] DSP3V3	>>	VCC
[1,2,3,4,5] VCC	>>	VCC
[1,2,3,4,5,7] GND	>>	GND

